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Advanced Digital Design : session 3 14 July 2020 : Making of SOC final part

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use traditional methods of logic design involving the drawing of logic diagrams when the digital circuit may contain thousands of gates. The reality is that today digital systems are designed by writing software in the form of hardware description languages (HDLs). The most common HDLs used today are VHDL and Verilog. Both are in widespread use.

Introduction to Digital Design Using Digilent FPGA Boards

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